

High Frequency Integrated Circuits The Cambridge Rf And Microwave Engineering Series

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RF-Frontend Design for Process-Variation-

Tolerant Receivers Pooyan Sakian 2012-02-22

This book discusses a number of challenges faced by designers of wireless receivers, given complications caused by the shrinking of electronic and mobile devices circuitry into ever-smaller sizes and the resulting complications on the manufacturability, production yield, and the end price of the products. The authors describe the impact of process technology on the performance of the end product and equip RF designers with countermeasures to cope with such problems. The mechanisms by which these problems arise are analyzed in detail and novel solutions are provided, including design guidelines for receivers with robustness to process variations and details of circuit blocks that obtain the required performance level. Describes RF receiver frontends and their building blocks from a system- and circuit-level perspective; Provides system-level analysis of a generic RF receiver frontend with robustness to process variations; Includes details of CMOS

circuit design at 60GHz and reconfigurable circuits at 60GHz; Covers millimeter-wave circuit design with robustness to process variations.

CMOS-MEMS Henry Baltes 2008-07-11

Planar Microwave Engineering Thomas H. Lee 2004-08-30 Modern wireless communications hardware is underpinned by RF and microwave design techniques. This insightful book contains a wealth of circuit layouts, design tips, and practical measurement techniques for building and testing practical gigahertz systems. The book covers everything you need to know to design, build, and test a high-frequency circuit. Microstrip components are discussed, including tricks for extracting good performance from cheap materials. Connectors and cables are also described, as are discrete passive components, antennas, low-noise amplifiers, oscillators, and frequency synthesizers. Practical measurement techniques are presented in detail, including the use of network analyzers, sampling oscilloscopes, spectrum analyzers, and noise figure meters.

Throughout the focus is practical, and many worked examples and design projects are included. There is also a CD-ROM that contains a variety of design and analysis programs. The book is packed with indispensable information for students taking courses on RF or microwave circuits and for practising engineers.

The Design of CMOS Radio-Frequency Integrated Circuits Thomas H. Lee 1998-01-13

This comprehensive and insightful book sets out in detail how to design gigahertz-speed radio-frequency integrated circuits in CMOS technology. Starting with a history of radio to establish a foundation and to differentiate the discrete era from the IC age, the book reviews passive RLC networks, the characteristics of IC components and transistor models. The design of high-frequency tuned and broadband amplifiers follows, with an emphasis on approximate methods that provide important design insight as a complement to simulation results. Key RF building blocks, such as low-noise amplifiers

(LNAs), mixers, power amplifiers, high spectral purity oscillators and frequency synthesizers are studied in detail. The book closes with an examination of transceiver architectures. With over 350 circuit diagrams and illustrations, and many homework problems, this will be an ideal textbook for anyone taking advanced undergraduate or graduate courses in RF electronics, as well as a useful reference for practising engineers.

The Design of CMOS Radio-Frequency Integrated Circuits Thomas H. Lee 2004 This book, first published in 2004, is an expanded and revised edition of Tom Lee's acclaimed RFIC text.

Radio Frequency Integrated Circuit Design John W. M. Rogers 2014-05-14 This newly revised and expanded edition of the 2003 Artech House classic, *Radio Frequency Integrated Circuit Design*, serves as an up-to-date, practical reference for complete RFIC know-how. The second edition includes numerous updates, including greater coverage of CMOS PA design,

RFIC design with on-chip components, and more worked examples with simulation results. By emphasizing working designs, this book practically transports you into the authors' own RFIC lab so you can fully understand the function of each design detailed in this book. Among the RFIC designs examined are RF integrated LC-based filters, VCO automatic amplitude control loops, and fully integrated transformer-based circuits, as well as image reject mixers and power amplifiers. If you are new to RFIC design, you can benefit from the introduction to basic theory so you can quickly come up to speed on how RFICs perform and work together in a communications device. A thorough examination of RFIC technology guides you in knowing when RFICs are the right choice for designing a communication device. This leading-edge resource is packed with over 1,000 equations and more than 435 illustrations that support key topics."

Wireless Technologies Krzysztof Iniewski

2017-12-19 Advanced concepts for wireless technologies present a vision of technology that is embedded in our surroundings and practically invisible. From established radio techniques like GSM, 802.11 or Bluetooth to more emerging technologies, such as Ultra Wide Band and smart dust motes, a common denominator for future progress is the underlying integrated circuit technology. *Wireless Technologies* responds to the explosive growth of standard cellular radios and radically different wireless applications by presenting new architectural and circuit solutions engineers can use to solve modern design problems. This reference addresses state-of-the-art CMOS design in the context of emerging wireless applications, including 3G/4G cellular telephony, wireless sensor networks, and wireless medical application. Written by top international experts specializing in both the IC industry and academia, this carefully edited work uncovers new design opportunities in body area networks, medical implants, satellite

communications, automobile radar detection, and wearable electronics. The book is divided into three sections: wireless system perspectives, chip architecture and implementation issues, and devices and technologies used to fabricate wireless integrated circuits. Contributors address key issues in the development of future silicon-based systems, such as scale of integration, ultra-low power dissipation, and the integration of heterogeneous circuit design style and processes onto one substrate. Wireless sensor network systems are now being applied in critical applications in commerce, healthcare, and security. This reference, which contains 25 practical and scientifically rigorous articles, provides the knowledge communications engineers need to design innovative methodologies at the circuit and system level. *The ESD Handbook* Steven H. Voldman
2021-03-02 A practical and comprehensive reference that explores Electrostatic Discharge (ESD) in semiconductor components and

electronic systems The ESD Handbook offers a comprehensive reference that explores topics relevant to ESD design in semiconductor components and explores ESD in various systems. Electrostatic discharge is a common problem in the semiconductor environment and this reference fills a gap in the literature by discussing ESD protection. Written by a noted expert on the topic, the text offers a topic-by-topic reference that includes illustrative figures, discussions, and drawings. The handbook covers a wide-range of topics including ESD in manufacturing (garments, wrist straps, and shoes); ESD Testing; ESD device physics; ESD semiconductor process effects; ESD failure mechanisms; ESD circuits in different technologies (CMOS, Bipolar, etc.); ESD circuit types (Pin, Power, Pin-to-Pin, etc.); and much more. In addition, the text includes a glossary, index, tables, illustrations, and a variety of case studies. Contains a well-organized reference that provides a quick review on a range of ESD topics

Fills the gap in the current literature by providing information from purely scientific and physical aspects to practical applications Offers information in clear and accessible terms Written by the accomplished author of the popular ESD book series Written for technicians, operators, engineers, circuit designers, and failure analysis engineers, The ESD Handbook contains an accessible reference to ESD design and ESD systems.

The VLSI Handbook Wai-Kai Chen 2018-10-03 For the new millenium, Wai-Kai Chen introduced a monumental reference for the design, analysis, and prediction of VLSI circuits: The VLSI Handbook. Still a valuable tool for dealing with the most dynamic field in engineering, this second edition includes 13 sections comprising nearly 100 chapters focused on the key concepts, models, and equations. Written by a stellar international panel of expert contributors, this handbook is a reliable, comprehensive resource for real answers to practical problems. It

emphasizes fundamental theory underlying professional applications and also reflects key areas of industrial and research focus. WHAT'S IN THE SECOND EDITION? Sections on... Low-power electronics and design VLSI signal processing Chapters on... CMOS fabrication Content-addressable memory Compound semiconductor RF circuits High-speed circuit design principles SiGe HBT technology Bipolar junction transistor amplifiers Performance modeling and analysis using SystemC Design languages, expanded from two chapters to twelve Testing of digital systems Structured for convenient navigation and loaded with practical solutions, The VLSI Handbook, Second Edition remains the first choice for answers to the problems and challenges faced daily in engineering practice.

Millimeter-Wave Circuits for 5G and Radar Gernot Hueber 2019-05-31 Discover the concepts, architectures, components, tools, and techniques needed to design millimeter-wave circuits for current and emerging wireless system

applications. Focusing on applications in 5G, connectivity, radar, and more, leading experts in radio frequency integrated circuit (RFIC) design provide a comprehensive treatment of cutting-edge physical-layer technologies for radio frequency (RF) transceivers - specifically RF, analog, mixed-signal, and digital circuits and architectures. The full design chain is covered, from system design requirements through to building blocks, transceivers, and process technology. Gain insight into the key novelties of 5G through authoritative chapters on massive MIMO and phased arrays, and learn about the very latest technology developments, such as FinFET logic process technology for RF and millimeter-wave applications. This is an essential reading and an excellent reference for high-frequency circuit designers in both academia and industry.

Modellierung der parasitären passiven Elemente in IGBT-Hochleistungsmodulen

Heeb, Michael 2014-01-01

Radio-Frequency Integrated-Circuit Engineering Cam Nguyen 2015-03-04 Radio-Frequency Integrated-Circuit Engineering addresses the theory, analysis and design of passive and active RFIC's using Si-based CMOS and Bi-CMOS technologies, and other non-silicon based technologies. The materials covered are self-contained and presented in such detail that allows readers with only undergraduate electrical engineering knowledge in EM, RF, and circuits to understand and design RFICs. Organized into sixteen chapters, blending analog and microwave engineering, Radio-Frequency Integrated-Circuit Engineering emphasizes the microwave engineering approach for RFICs. • Provides essential knowledge in EM and microwave engineering, passive and active RFICs, RFIC analysis and design techniques, and RF systems vital for RFIC students and engineers • Blends analog and microwave engineering approaches for RFIC design at high frequencies • Includes problems at the end of each chapter

Wireless Radio-Frequency Standards and System Design: Advanced Techniques

Cornetta, Gianluca 2012-01-31 Radio-frequency (RF) integrated circuits in CMOS technology are gaining increasing popularity in the commercial world, and CMOS technology has become the dominant technology for applications such as GPS receivers, GSM cellular transceivers, wireless LAN, and wireless short-range personal area networks based on IEEE 802.15.1 (Bluetooth) or IEEE 802.15.4 (ZigBee) standards. Furthermore, the increasing interest in wireless technologies and the widespread of wireless communications has prompted an ever increasing demand for radio frequency transceivers. *Wireless Radio-Frequency Standards and System Design: Advanced Techniques* provides perspectives on radio-frequency circuit and systems design, covering recent topics and developments in the RF area. Exploring topics such as LNA linearization, behavioral modeling and co-simulation of analog and mixed-signal complex

blocks for RF applications, integrated passive devices for RF-ICs and baseband design techniques and wireless standards, this is a comprehensive reference for students as well as practicing professionals.

ESD Steven H. Voldman 2006-11-02 With the growth of high-speed telecommunications and wireless technology, it is becoming increasingly important for engineers to understand radio frequency (RF) applications and their sensitivity to electrostatic discharge (ESD) phenomena. This enables the development of ESD design methods for RF technology, leading to increased protection against electrical overstress (EOS) and ESD. *ESD: RF Technology and Circuits: Presents methods for co-synthesizing ESD networks for RF applications to achieve improved performance and ESD protection of semiconductor chips; discusses RF ESD design methods of capacitance load transformation, matching network co-synthesis, capacitance shunts, inductive shunts, impedance isolation, load cancellation methods,*

distributed loads, emitter degeneration, buffering and ballasting; examines ESD protection and design of active and passive elements in RF complementary metal-oxide-semiconductor (CMOS), RF laterally-diffused metal oxide semiconductor (LDMOS), RF BiCMOS Silicon Germanium (SiGe), RF BiCMOS Silicon Germanium Carbon (SiGeC), and Gallium Arsenide technology; gives information on RF ESD testing methodologies, RF degradation effects, and failure mechanisms for devices, circuits and systems; highlights RF ESD mixed-signal design integration of digital, analog and RF circuitry; sets out examples of RF ESD design computer aided design methodologies; covers state-of-the-art RF ESD input circuits, as well as voltage-triggered to RC-triggered ESD power clamps networks in RF technologies, as well as off-chip protection concepts. Following the authors series of books on ESD, this book will be a thorough overview of ESD in RF technology for RF semiconductor chip and ESD engineers. Device

and circuit engineers working in the RF domain, and quality, reliability and failure analysis engineers will also find it a valuable reference in the rapidly growing area of RF ESD design. In addition, it will appeal to graduate students in RF microwave technology and RF circuit design.

Microwave Electronics Giovanni Ghione 2017-11-30 A self-contained guide to microwave electronics, covering passive and active components, linear, low-noise and power amplifiers, microwave measurements, and CAD techniques. It is the ideal text for graduate and senior undergraduate students taking courses in microwave and radio-frequency electronics, as well as professional microwave engineers.

Remote Powering and Data Communication for Implanted Biomedical Systems Enver Gurhan Kilinc 2015-09-17 This book describes new circuits and systems for implantable biomedical applications and explains the design of a batteryless, remotely-powered implantable micro-system, designed for long-term patient

monitoring. Following new trends in implantable biomedical applications, the authors demonstrate a system which is capable of efficient, remote powering and reliable data communication. Novel architecture and design methodologies are used to transfer power with a low-power, optimized inductive link and data is transmitted by a reliable communication link. Additionally, an electro-mechanical solution is presented for tracking and monitoring the implantable system, while the patient is mobile.

LNA-ESD Co-Design for Fully Integrated

CMOS Wireless Receivers Paul Leroux
2006-03-30 LNA-ESD Co-Design for Fully Integrated CMOS Wireless Receivers fits in the quest for complete CMOS integration of wireless receiver front-ends. With a combined discussion of both RF and ESD performance, it tackles one of the final obstacles on the road to CMOS integration. The book is conceived as a design guide for those actively involved in the design of CMOS wireless receivers. The book starts with a

comprehensive introduction to the performance requirements of low-noise amplifiers in wireless receivers. Several popular topologies are explained and compared with respect to future technology and frequency scaling. The ESD requirements are introduced and related to the state-of-the-art protection devices and circuits. LNA-ESD Co-Design for Fully Integrated CMOS Wireless Receivers provides an extensive theoretical treatment of the performance of CMOS low-noise amplifiers in the presence of ESD-protection circuitry. The influence of the ESD-protection parasitics on noise figure, gain, linearity, and matching are investigated. Several RF-ESD co-design solutions are discussed allowing both high RF-performance and good ESD-immunity for frequencies up to and beyond 5 GHz. Special attention is also paid to the layout of both active and passive components. LNA-ESD Co-Design for Fully Integrated CMOS Wireless Receivers offers the reader intuitive insight in the LNA's behavior, as well as the necessary

mathematical background to optimize its performance. All material is experimentally verified with several CMOS implementations, among which a fully integrated GPS receiver front-end. The book is essential reading for RF design engineers and researchers in the field and is also suitable as a text book for an advanced course on the subject.

Fast Techniques for Integrated Circuit

Design Mikael Sahrling 2019-08-15 Learn how to use estimation techniques to solve real-world IC design problems and accelerate design processes with this practical guide.

High-Frequency Integrated Circuits Sorin Voinigescu 2013-02-28 A transistor-level, design-intensive overview of high speed and high frequency monolithic integrated circuits for wireless and broadband systems from 2 GHz to 200 GHz, this comprehensive text covers high-speed, RF, mm-wave, and optical fibre circuits using nanoscale CMOS, SiGe BiCMOS, and III-V technologies. Step-by-step design

methodologies, end-of chapter problems, and practical simulation and design projects are provided, making this an ideal resource for senior undergraduate and graduate courses in circuit design. With an emphasis on device-circuit topology interaction and optimization, it gives circuit designers and students alike an in-depth understanding of device structures and process limitations affecting circuit performance.

[Performance Optimization Techniques in Analog, Mixed-Signal, and Radio-Frequency Circuit Design](#)

Fakhfakh, Mourad 2014-10-31 Improving the performance of existing technologies has always been a focal practice in the development of computational systems. However, as circuitry is becoming more complex, conventional techniques are becoming outdated and new research methodologies are being implemented by designers. Performance Optimization Techniques in Analog, Mix-Signal, and Radio-Frequency Circuit Design features recent advances in the engineering of integrated

systems with prominence placed on methods for maximizing the functionality of these systems. This book emphasizes prospective trends in the field and is an essential reference source for researchers, practitioners, engineers, and technology designers interested in emerging research and techniques in the performance optimization of different circuit designs.

RFID Systems Miodrag Bolic 2010-09-23 This book provides an insight into the 'hot' field of Radio Frequency Identification (RFID) Systems In this book, the authors provide an insight into the field of RFID systems with an emphasis on networking aspects and research challenges related to passive Ultra High Frequency (UHF) RFID systems. The book reviews various algorithms, protocols and design solutions that have been developed within the area, including most recent advances. In addition, authors cover a wide range of recognized problems in RFID industry, striking a balance between theoretical and practical coverage. Limitations of the

technology and state-of-the-art solutions are identified and new research opportunities are addressed. Finally, the book is authored by experts and respected researchers in the field and every chapter is peer reviewed. Key Features: Provides the most comprehensive analysis of networking aspects of RFID systems, including tag identification protocols and reader anti-collision algorithms Covers in detail major research problems of passive UHF systems such as improving reading accuracy, reading range and throughput Analyzes other "hot topics" including localization of passive RFID tags, energy harvesting, simulator and emulator design, security and privacy Discusses design of tag antennas, tag and reader circuits for passive UHF RFID systems Presents EPCGlobal architecture framework, middleware and protocols Includes an accompanying website with PowerPoint slides and solutions to the problems <http://www.site.uottawa.ca/~mbolic/RFIDBook/> This book will be an invaluable guide for

researchers and graduate students in electrical engineering and computer science, and researchers and developers in telecommunication industry.

Toward 5G Software Defined Radio Receiver

Front-Ends Silvian Spiridon 2016-06-21 This book introduces a new intuitive design methodology for the optimal design path for next-generation software defined radio front-ends (SDRXs). The methodology described empowers designers to "attack" the multi-standard environment in a parallel way rather than serially, providing a critical tool for any design methodology targeting 5G circuits and systems. Throughout the book the SDRX design follows the key wireless standards of the moment (i.e., GSM, WCDMA, LTE, Bluetooth, WLAN), since a receiver compatible with these standards is the most likely candidate for the first design iteration in a 5G deployment. The author explains the fundamental choice the designer has to make regarding the optimal channel selection: how

much of the blockers/interferers will be filtered in the analog domain and how much will remain to be filtered in the digital domain. The system-level analysis the author describes entails the direct sampling architecture is treated as a particular case of mixer-based direct conversion architecture. This allows readers give a power consumption budget to determine how much filtering is required on the receive path, by considering the ADC performance characteristics and the corresponding blocker diagram.

Digital Radio System Design

Grigorios Kalivas 2009-10-23 A systematic explanation of the principles of radio systems, Digital Radio System Design offers a balanced treatment of both digital transceiver modems and RF front-end subsystems and circuits. It provides an in-depth examination of the complete transceiver chain which helps to connect the two topics in a unified system concept. Although the book tackles such diverse fields it treats them in sufficient depth to give the designer a solid foundation and an

implementation perspective. Covering the key concepts and factors that characterise and impact radio transmission and reception, the book presents topics such as receiver design, noise and distortion. Information is provided about more advanced aspects of system design such as implementation losses due to non-idealities. Providing vivid examples, illustrations and detailed case-studies, this book is an ideal introduction to digital radio systems design. Offers a balanced treatment of digital modem and RF front-end design concepts for complete transceivers Presents a diverse range of topics related to digital radio design including advanced transmission and synchronization techniques with emphasis on implementation Provides guidance on imperfections and non-idealities in radio system design Includes detailed design case-studies incorporating measurement and simulation results to illustrate the theory in practice

Microwave Circuits for 24 GHz Automotive Radar

in Silicon-based Technologies Vadim Issakov
2010-08-05 There are continuous efforts focussed on improving road traffic safety worldwide. Numerous vehicle safety features have been invented and standardized over the past decades. Particularly interesting are the driver assistance systems, since these can considerably reduce the number of accidents by supporting drivers' perception of their surroundings. Many driver assistance features rely on radar-based sensors. Nowadays the commercially available automotive front-end sensors are comprised of discrete components, thus making the radar modules highly-priced and suitable for integration only in premium class vehicles. Realization of low-cost radar front-end circuits would enable their implementation in inexpensive economy cars, considerably contributing to traffic safety. Cost reduction requires high-level integration of the microwave front-end circuitry, specifically analog and digital circuit blocks co-located on a single chip. - cent

developments of silicon-based technologies, e.g. CMOS and SiGe:C bipolar, make them suitable for realization of microwave sensors. Additionally, these technologies offer the necessary integration capability. However, the required output power and temperature stability, necessary for automotive radar sensor products, have not yet been achieved in standard digital CMOS technologies. On the other hand, SiGe bipolar technology offers excellent high-frequency characteristics and necessary output power for automotive applications, but has lower potential for realization of digital blocks than CMOS.

High-Frequency Integrated Circuits South Asia Edition Sorin Voinigescu 2017-04-01 A transistor-level, design-intensive overview of high speed and high frequency monolithic integrated circuits for wireless and broadband systems from 2 GHz to 200 GHz, this comprehensive text covers high-speed, RF, mm-wave, and optical fibre circuits using nanoscale CMOS, SiGe BiCMOS, and III-V technologies. Step-by-step design

methodologies, end-of chapter problems, and practical simulation and design projects are provided, making this an ideal resource for senior undergraduate and graduate courses in circuit design. With an emphasis on device-circuit topology interaction and optimization, it gives circuit designers and students alike an in-depth understanding of device structures and process limitations affecting circuit performance.

Ultra High-Speed CMOS Circuits Sam Gharavi 2011-09-25 The book covers the CMOS-based millimeter wave circuits and devices and presents methods and design techniques to use CMOS technology for circuits operating beyond 100 GHz. Coverage includes a detailed description of both active and passive devices, including modeling techniques and performance optimization. Various mm-wave circuit blocks are discussed, emphasizing their design distinctions from low-frequency design methodologies. This book also covers a device-oriented circuit design technique that is essential for ultra high speed

circuits and gives some examples of device/circuit co-design that can be used for mm-wave technology.

Electromagnetics for High-Speed Analog and Digital Communication Circuits Ali M. Niknejad
2007-02-22 Modern communications technology demands smaller, faster and more efficient circuits. This book reviews the fundamentals of electromagnetism in passive and active circuit elements, highlighting various effects and potential problems in designing a new circuit. The author begins with a review of the basics - the origin of resistance, capacitance, and inductance - then progresses to more advanced topics such as passive device design and layout, resonant circuits, impedance matching, high-speed switching circuits, and parasitic coupling and isolation techniques. Using examples and applications in RF and microwave systems, the author describes transmission lines, transformers, and distributed circuits. State-of-the-art developments in Si based broadband

analog, RF, microwave, and mm-wave circuits are reviewed. With up-to-date results, techniques, practical examples, illustrations and worked examples, this book will be valuable to advanced undergraduate and graduate students of electrical engineering, and practitioners in the IC design industry. Further resources for this title are available at www.cambridge.org/9780521853507.

High-Frequency Integrated Circuits Sorin Voinigescu 2013-02-28 A transistor-level, design-intensive overview of high speed and high frequency monolithic integrated circuits for wireless and broadband systems from 2 GHz to 200 GHz, this comprehensive text covers high-speed, RF, mm-wave and optical fiber circuits using nanoscale CMOS, SiGe BiCMOS and III-V technologies. Step-by-step design methodologies, end-of-chapter problems and practical simulation and design projects are provided, making this an ideal resource for senior undergraduate and graduate courses in circuit

design. With an emphasis on device-circuit topology interaction and optimization, it gives circuit designers and students alike an in-depth understanding of device structures and process limitations affecting circuit performance.

Telecommunication Electronics Dante Del Corso 2020-02-29 This practical, hands-on resource describes functional units and circuits of telecommunication systems. The functions characterizing these systems, including RF amplifiers (both low noise and power amplifiers), signal sources, mixers and phase lock loops, are explored from an operational level viewpoint. And as all functions are migrating to digital implementations, this book describes functional units and circuits of telecommunication systems (with radio, wire, or optical links), from functional level viewpoint to the circuit details and examples. The structure of a radio transceiver is described and a view of all functional units, including migration to SDR (Software Defined Radio) is provided. Chapters include a functional

identification of the units described and analysis of possible circuit solutions and analysis of error sources. The sequence reflects the actual design procedure: functional identification, search and analysis of solutions, and critical review to provide an understanding of the various solutions and tradeoffs, with guidelines for design and/or selection of proper functional units.

Optimizing Wireless/RF Circuits John D. Lenk 1999 Annotation "Described by the author as "a crash course" in wireless/RF circuits, this book enables experimenters as well as engineers to get peak performance from off-the-shelf integrated circuits (ICs) in wireless and radio frequency (RF) design." "Packed with step-by-step instructions, illustrations, and diagrams, and written by one of the world's leading electronics authors, *Optimizing Wireless/RF Circuits* is a book that focuses on the circuits most needed in the growing number of wireless consumer products." -BOOK JACKET. Title Summary field provided by Blackwell North America, Inc. All Rights Reserved.

Wireless Transceiver Design Ariel Luzzatto
2016-09-08 Building upon the success of the first edition (2007), *Wireless Transceiver Design 2nd Edition* is an accessible textbook that explains the concepts of wireless transceiver design in detail. The architectures and the detailed design of both traditional and advanced all-digital wireless transceivers are discussed in a thorough and systematic manner, while carefully watching out for clarity and simplicity. Many practical examples and solved problems at the end of each chapter allow students to thoroughly understand the mechanisms involved, to build confidence, and enable them to readily make correct and practical use of the applicable results and formulas. From the instructors' perspective, the book will enable the reader to build courses at different levels of depth, starting from the basic understanding, whilst allowing them to focus on particular elements of study. In addition to numerous fully-solved exercises, the authors include actual exemplary examination papers for

instructors to use as a reference format for student evaluation. The new edition has been adapted with instructors/lecturers, graduate/undergraduate students and RF engineers in mind. Non-RF engineers looking to acquire a basic understanding of the main related RF subjects will also find the book invaluable.

Design of High-Speed Communication Circuits
RF and Time-domain Techniques for Evaluating Novel Semiconductor Transistors Keith A. Jenkins
2021-12-15 This book presents a variety of techniques using high-frequency (RF) and time-domain measurements to understand the electrical performance of novel, modern transistors made of materials such as graphene, carbon nanotubes, and silicon-on-insulator, and using new transistor structures. The author explains how to use conventional RF and time-domain measurements to characterize the performance of the transistors. In addition, he explains how novel transistors may be subject to

effects such as self-heating, period-dependent output, non-linearity, susceptibility to short-term degradation, DC-invisible structural defects, and a different response to DC and transient inputs. Readers will understand that in order to fully understand and characterize the behavior of a novel transistor, there is an arsenal of dynamic techniques available. In addition to abstract concepts, the reader will learn of practical tips required to achieve meaningful measurements, and will understand the relationship between these measurements and traditional, conventional DC characteristics.

Radio Frequency Integrated Circuits and Technologies Frank Ellinger 2008-09-11 The striking feature of this book is its coverage of the upper GHz domain. However, the latest technologies, applications and broad range of circuits are discussed. Design examples are provided including cookbook-like optimization strategies. This state-of-the-art book is valuable for researchers as well as for engineers in

industry. Furthermore, the book serves as fruitful basis for lectures in the area of IC design.

An Introduction to Radio Frequency Engineering Christopher Coleman 2004-05-13 Originally published in 2004, this book provides a detailed introduction to radio frequency (RF) engineering, using a straightforward and easily understood approach combined with numerous worked examples, illustrations and homework problems. The author focuses on minimising the mathematics needed to grasp the subject while providing a solid theoretical foundation for the student. Emphasis is also placed on the practical aspects of radio engineering. The book provides a broad coverage of RF systems, circuit design, antennas, propagation and digital techniques. It will provide an excellent introduction to the subject for graduate students, researchers and practising engineers.

RF Circuit Design Richard C. Li 2012-08-24 Summarizes the schemes and technologies in RF circuit design, describes the basic parameters of

an RF system and the fundamentals of RF system design, and presents an introduction of the individual RF circuit block design. Forming the backbone of today's mobile and satellite communications networks, radio frequency (RF) components and circuits are incorporated into everything that transmits or receives a radio wave, such as mobile phones, radio, WiFi, and walkie talkies. RF Circuit Design, Second Edition immerses practicing and aspiring industry professionals in the complex world of RF design. Completely restructured and reorganized with new content, end-of-chapter exercises, illustrations, and an appendix, the book presents integral information in three complete sections: Part One explains the different methodologies between RF and digital circuit design and covers voltage and power transportation, impedance matching in narrow-band case and wide-band case, gain of a raw device, measurement, and grounding. It also goes over equipotentiality and current coupling on ground surface, as well as

layout and packaging, manufacturability of product design, and radio frequency integrated circuit (RFIC). Part Two includes content on the main parameters and system analysis in RF circuit design, the fundamentals of differential pair and common-mode rejection ratio (CMRR), Balun, and system-on-a-chip (SOC). Part Three covers low-noise amplifier (LNA), power amplifier (PA), voltage-controlled oscillator (VCO), mixers, and tunable filters. RF Circuit Design, Second Edition is an ideal book for engineers and managers who work in RF circuit design and for courses in electrical or electronic engineering. *mm-Wave Silicon Power Amplifiers and Transmitters* Hossein Hashemi 2016-04-04 Build high-performance, spectrally clean, energy-efficient mm-wave power amplifiers and transmitters with this cutting-edge guide to designing, modeling, analysing, implementing and testing new mm-wave systems. Suitable for students, researchers and practicing engineers, this self-contained guide provides in-depth

coverage of state-of-the-art semiconductor devices and technologies, linear and nonlinear power amplifier technologies, efficient power combining systems, circuit concepts, system architectures and system-on-a-chip realizations. The world's foremost experts from industry and academia cover all aspects of the design process, from device technologies to system architectures. Accompanied by numerous case studies highlighting practical design techniques, tradeoffs and pitfalls, this is a superb resource for those working with high-frequency systems.

Microwave Circuit Design Using Linear and Nonlinear Techniques George D. Vendelin

2021-05-04 Four leaders in the field of microwave circuit design share their newest insights into the latest aspects of the technology. The third edition of *Microwave Circuit Design Using Linear and Nonlinear Techniques* delivers an insightful and complete analysis of microwave circuit design, from their intrinsic and circuit properties to circuit design techniques for

maximizing performance in communication and radar systems. This new edition retains what remains relevant from previous editions of this celebrated book and adds brand-new content on CMOS technology, GaN, SiC, frequency range, and feedback power amplifiers in the millimeter range region. The third edition contains over 200 pages of new material. The distinguished engineers, academics, and authors emphasize the commercial applications in telecommunications and cover all aspects of transistor technology. Software tools for design and microwave circuits are included as an accompaniment to the book. In addition to information about small and large-signal amplifier design and power amplifier design, readers will benefit from the book's treatment of a wide variety of topics, like: An in-depth discussion of the foundations of RF and microwave systems, including Maxwell's equations, applications of the technology, analog and digital requirements, and elementary

definitions A treatment of lumped and distributed elements, including a discussion of the parasitic effects on lumped elements Descriptions of active devices, including diodes, microwave transistors, heterojunction bipolar transistors, and microwave FET Two-port networks, including S-Parameters from SPICE analysis and the derivation of transducer power gain Perfect for microwave integrated circuit designers, the third edition of *Microwave Circuit Design Using Linear and Nonlinear Techniques* also has a place on the bookshelves of electrical engineering researchers and graduate students. It's comprehensive take on all aspects of transistors by world-renowned experts in the field places this book at the vanguard of microwave circuit design research. *Adaptive Multi-Standard RF Front-Ends* Vojkan Vidojkovic 2008-02-07 This book investigates solutions, benefits, limitations, and costs associated with multi-standard operation of RF front-ends and their ability to adapt to variable radio environments. Next, it highlights the

optimization of RF front-ends to allow maximum performance within a certain power budget, while targeting full integration. Finally, the book investigates possibilities for low-voltage, low-power circuit topologies in CMOS technology. *Radio Frequency Integrated Circuit Design for Cognitive Radio Systems* Amr Fahim 2015-03-03 This book fills an information gap on cognitive radios, since the discussion focuses on the implementation issues that are unique to cognitive radios and how to solve them at both the architecture and circuit levels. This is the first book to describe in detail cognitive radio systems, as well as the circuit implementation and architectures required to implement such systems. Throughout the book, requirements and constraints imposed by cognitive radio systems are emphasized when discussing the circuit implementation details. This is a valuable reference for anybody with background in analog and radio frequency (RF) integrated circuit design, needing to learn more about integrated

circuits requirements and implementation for cognitive radio systems.